

Celestron Test System

Flexible bench-top TLP / VF-TLP test system

The Celestron Test System is a two-terminal bench-top TLP / VF-TLP test system for fast, accurate, reliable, and affordable characterization of advanced semiconductor structures.

- Wafer and package level TLP testing
- Integrated WINDOWS® - based system controller
- High current TLP pulse generator
- Integrated +/-200V source/meter unit
- Optional bias supplies (up to 5) under computer control for powered testing and measurements
- Can be interfaced with semiautomatic probers
- Advanced, intuitive software for control and report generation
- Small bench top footprint

Flexible test capabilities

The Thermo Scientific™ Celestron™ TLP/VF-TLP Test System can be configured for Standard TLP and VF-TLP for testing at the wafer level and/or package level. Optional probes can also be used to measure signals on pins or pads other than the ones being stressed.

Unsurpassed test control

The Celestron Test System software is the most comprehensive in the industry, and utilizes graphics to assist in system setup and connection to the DUT. During test operations, it displays the recorded TLP pulse voltage and current waveforms, compiled pulsed I-V curve, leakage current measurements, and DC I-V curve trace data. The operator can select the range of test voltages (stress pulses), pulse polarity, leakage and curve trace parameters. The position and duration of the measurement window within the TLP pulse can also be selected, and modified after the data is collected.



Standard TLP configuration

Pulse widths from 30 to 500ns	<p>Integrated in the pulser box, the standard TLP Pulse width is 100ns</p> <p>Additional pulse widths can be added externally through a cable change ranging from 30 to 500ns</p> <p>Optional computer controlled pulse width with 3 selectable pulse widths</p>
Rise times available	<p>Standard rise time is 200ps</p> <p>Optional rise times from 500ps to 10ns</p> <p>Controlled by optional external rise time filters</p> <p>2nS and 10nS external rise time filter included</p> <p>Optional computer controlled rise time with 3 selectable rise times</p> <p>Note: 500 MHz or faster oscilloscope is required for TLP measurements</p> <p>1 GHz or grater scope is recommended for measurements with rise times of less than 1ns</p>
Support of all TLP configurations	<p>Optional changing of configuration for 25 to 500 ohm delivery impedances</p> <p>Support for all configurations described in the ESD Association's TLP Standard Test Method</p> <p>Both wafer level and package testing at all impedances</p>
Time Domain Reflection with Overlapped pulses (TDR-O)	<p>Standard TLP configuration for pulse widths above 30ns</p> <p>50 ohm delivery impedance</p> <p>Allows "adaptive ranging" of oscilloscope for improved accuracy</p> <p>Maximum DUT current of 20A into 50 ohm load and 40A into a short</p> <p>Maximum DUT open circuit voltage to 2000V</p> <p>Multiple grounds, multiple biased pins/pads, multiple ground current path measurement, and multiple voltage node measurements are possible</p>
Time Domain Reflection with Separated pulses (TDR-S)	<p>50 ohm delivery impedance</p> <p>Standard TLP confi guration for pulse widths under 50ns or when measurement pod cannot be placed close to the DUT</p> <p>DUT current of 20A into 50 ohm load and 40A into a short</p> <p>Maximum DUT open circuit voltage to 2000V</p> <p>Multiple grounds possible</p>
Optional Time Domain Transmission	<p>25 ohm delivery impedance</p> <p>Maximum DUT current of 20A into 25 ohm load and 40A into a short</p> <p>Maximum DUT open circuit voltage to 1000V</p> <p>Multiple grounds, multiple biased pins/pads, multiple ground current path measurement, and multiple voltage node measurements are possible</p>
Optional Time Domain Reflection and Transmission (TDRT)	<p>100 ohm delivery impedance</p> <p>Maximum DUT current of 13.3A into 50 ohm load and 20A into a short</p> <p>Maximum DUT open circuit voltage to 2000V</p>
Optional 500 ohm (current source) TLP	<p>High-Z TDRT mode for high efficiency</p> <p>Computer switching between 500 ohm and 50 ohm</p> <p>TDR-O</p> <p>Maximum DUT current of 3.0A into 50 ohm load and 3.3A into a short</p> <p>Maximum DUT open circuit voltage to 2000V</p>
48 pin DIP DUT board (one included)	<p>50 ohm coaxial probe pulse delivery</p> <p>Tungsten ground probe</p> <p>Needles are replaceable</p>
Wafer probe (one set included)	<p>50 ohm coaxial probe pulse delivery</p>

Standard TLP / VF-TLP configuration

Wafer and package level TLP testing	Flexibility
Integrated system controller	No need for a dedicated computer Windows® operating system
TLP pulse generator	Cable-generated pulse Dual polarity pulses under computer control Peak pulse current 40A Open circuit voltage of up to 2000V Voltage Resolution: Range 1: $0 < V < V_{\text{threshold1}}$ step resolution = 0.25V +/- 0.1V +/- 15% Range 2: $V_{\text{threshold1}} < V < V_{\text{threshold2}}$ step resolution = 0.5V +/- 0.25V +/-15% Range 3: $V > V_{\text{threshold2}}$ step resolution = 2.5V +/- 1.25V +/-15% Note: $V_{\text{threshold1}}$ is ~ 40V, $V_{\text{threshold2}}$ is ~160V
Current and voltage probes	Included in a small pod for flexibility
Integrated source/meter unit	Curve Tracing to ± 200V Leakage measurements down to 50pA DUT failure detection based on leakage or voltage with forced current or both
Optional bias supplies	Up to 5 under computer control for powered testing and measurements Kit includes Bias Tee and cables
Can be interfaced with semi-automatic probers	Flexibility, cost-savings, ease of test
Advanced software	Exacting test control, report generation
Platform	TLP test capability (TDR 50 ohm standard): Pulse single pin Can ground one or multiple pins (TDR) Semiautomatic operation Customer supplied oscilloscope: 500 MHz min. BW for Standard TLP 2.5 GHz min. BW for VF-TLP Wafer interface kit requires customer-supplied prober station and micro positioners
Transmission Line Pulse (TLP / VF-TLP)	Designed in compliance with the current ESD Association Standard Test Method Documents (ANSI/ESD STM 5.5.1-2016)
Operating Range	Operating Temperature +5°C to +44°C (+40°F to +112°F) Non-operating temperature 4°C to +60°C (+40°F to +140°F) Humidity Range 10-80% non-condensing
Dimensions	56 cm (22 in) x 61 cm (32 in) D x 81.3 cm (32 in) H
Power Requirements	System 100-240 VAC, 10A, 50/60 Hz Computer and Monitor 100-240 VAC, 6.5A, 50/60 Hz
Options	Bias supply up to ±50V and 2A DUT test fixture boards (may also use Keithley 24XX or Keithley 2600) Semi-automatic prober interface software VF-TLP charge lines: 1.2 to 10ns Custom time delay/TDR-S cables: 10ns to 500ns Variable pulse widths and rise times from 0.2 to 10ns Computer controlled switching between 3 pulse widths Note: Option limits short circuit current to 20A Computer controlled switching between 3 selectable rise times Coaxial and solid wafer probe needles Kelvin measurement kit to remove wafer contact resistance Additional ground wafer probes (for TDR and TDT only) Wafer probes of various radius tips in tungsten and copper

Standard VF-TLP configuration

Pulse widths from 1.2 to 10ns	Selected by cable change at front of system Standard widths are 1.2, 2.5 and 5ns (others optional)
Maximum pulse current	15A into a 50 ohm load Approximately 30A into a short circuit
Maximum open circuit voltage	1500V
DUT testing	Wafer level and package-level
Rise times from 200ps to 2ns	Controlled by optional filters Standard rise times are 200 and 300ps 2ns external rise time filter included
Time Domain Reflection (TDR) with Separated pulses standard (TDR-S)	50 ohm delivery impedance Uses high frequency GS or GSG needle assembly (sold separately)
Time Domain Reflection and Transmission standard (TDRT)	100 ohm delivery impedance Flexible independent needles for testing wafer or packaged parts
Signal merge box technology	Converts TDR-S signals to TDR-O Allows "adaptive ranging" to reduce voltage noise
VF-TLP current and voltage measurement pod	Bandwidths > 2 GHz
VF-TLP DUT boards	VF-TLP DUT board for DIP device included Calibration standards included on DUT board
Typical pulse characteristics	Overshoot vs. rise time: < 10% at 200ps and < 5% at 300ps Ringing amplitude peak-to-peak including overshoot < 20% at 200ps and < 10% at 300ps Settling time (ring duration) to $\pm 2\%$: < 1 ns at < 300ps rise time and < 500ps at > 300ps rise time Fall times between 1 and 2 times the rise time

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